

# Compositionality and monadic effects

Challenges and perspectives for  
certification of real-time guarantees

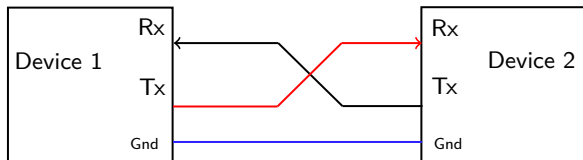
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# Running example

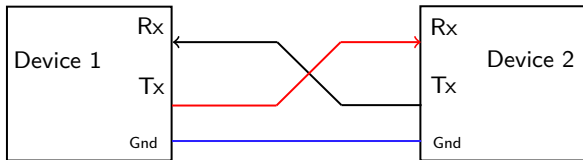
## Universal Asynchronous Receiver Transmitter (UART)



- ▶ communication between two devices without shared clock (Asynchronous);
- ▶ pre-defined speed (baudrates) between Device 1 and Device 2;
- ▶ rules to send and receive symbols.

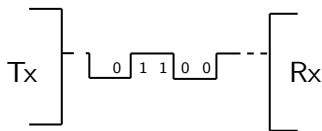
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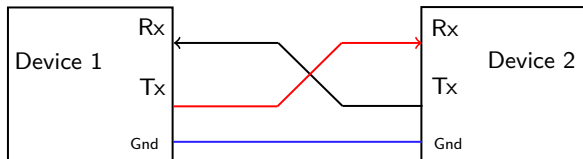
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Example: send the sequence *00110*



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- ▶ rules to send and receive symbols.

### Motivations:

- ▶ implement a bit-banging receiver and transmitter (in software)  
- how fast to sample?;
- ▶ verify that the communication follows the UART protocol.

## Pseudo-code

### Transmitter:

```
baudrate := 9600;
char s[8] :=
    {0,1,1,0,0,0,1,0};
int c := 0;
TxSend(0);
wait 1/baudrate;
while c < 8 do
    TxSend(s[c]);
    wait 1/baudrate;
    c := c+1;
od
```

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### Receiver:

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baudrate := 9600;
char s[];
int c := 0;
while true do
    if RxLow then
        wait 1/baudrate;
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- ▶ wait and RxLow are time sensitive instructions.

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How to give a denotational and compositional semantics to prove real time properties of the composition?

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## Composition

Capture functionally and compositionally the propagation of time, i.e., given two time sensitive C programs  $P_1$  and  $P_2$ ,

$$\llbracket P_1 |> P_2 \rrbracket = f(\llbracket P_1 \rrbracket, \llbracket P_2 \rrbracket)$$

where

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Goal:

- ▶ define sequential composition and parallel composition via the  $|>$  operation.

# Research plan

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Extend semantic preservation theorem (CompCert).

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Real programs have effects, such as time.
2. Certified compilation  
Extend semantic preservation theorem (CompCert).
3. Application  
Reference implementation for UART protocol.

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Real programs execute on a physical hardware, and each instruction has an effect (time, energy, actuation, memory, ...).



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To get back functional features (composition), extend the nature of a function to include *effects* (e.g., define  $f^\dagger : A \rightarrow MB$  for  $f : A \rightarrow B$  where  $M$  adds effects to  $B$ ). Then,

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What structure for reactive programs with physical effects, such as time passing?

## Compositional semantics

Define reactive processes with denotational semantics of type  
 $WA \rightarrow MB$ ,

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<sup>2</sup>Notions of computations and monads, E. Moggi, 1991

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Composition is given by  $\llbracket P_1 |> P_2 \rrbracket : WA \rightarrow MC$ , with

- ▶ two processes  $P_1 : WA \rightarrow MB$  and  $P_2 : WB \rightarrow MC$ ; and
- ▶ the proof of distributivity<sup>3</sup> of  $W$  over  $M$ .

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## Compositional semantics

Example of compositional semantics for reactive programs:

- ▶ with errors:  $A^+ \rightarrow B + 1$ ,  
distributive law between  $_+^+$  and  $_ + 1$  proved in Coq;
- ▶ with non-determinism:  $A^+ \rightarrow \mathcal{P}(B) \setminus \{\emptyset\}$ ,  
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As a result, the distributivity law induces a bi-Kleisli category with the following composition:

$$\frac{\llbracket P_1 \rrbracket : A^+ \rightarrow B + 1, \quad \llbracket P_2 \rrbracket : B^+ \rightarrow C + 1}{\llbracket P_1 |> P_2 \rrbracket = \llbracket P_2 \rrbracket \circ \llbracket P_1 \rrbracket : A^+ \rightarrow C + 1}$$

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Note that:

- ▶ induced composition  $\circ$  is associative and has an identity;
- ▶ we can prove equalities such as  $\llbracket P_1 \mid > P_2 \rrbracket = \llbracket P_2 \mid > P_1 \rrbracket$ .

# Compositional semantics

Let

- ▶  $\mathbb{N}^+$  co-monad;
- ▶  $\mathbb{N} + 1$  monad;
- ▶ processes  $P_1, P_2 : \mathbb{N}^+ \rightarrow \mathbb{N} + 1$ .

Example,

$$\frac{\begin{array}{ccc} \frac{[0]}{[0]} & \frac{[1]}{[1]} & \frac{[0]}{[0]} \\ \frac{[0]}{[0]} & \frac{[1]}{[1]} & \frac{[0]}{[0]} \end{array}}{\frac{[0]}{[0]} \quad 1 \quad \frac{[1]}{[1]} \quad 0 \quad \frac{[0]}{[0]} \quad 0}{} \quad \frac{[[P_1]] \quad [[P_2]] \quad [[P_2]] \circ [[P_1]]}{[0] \quad 1 \quad [1] \quad 0 \quad [0] \quad 0}}$$

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<sup>4</sup>Implementing Hybrid Semantics: From Functional to Imperative, S. Goncharov, R. Neves, J. Proença, 2020

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$[0, 0, 1, 1]$	0	$[1, 1, 0]$	1	$[0, 0, 1, 1]$	1

On-going work:

- ▶ choose monad and co-monad to model time propagation<sup>4</sup>; and
- ▶ add physical processes to model network delays.

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# Compilation

Goal: preserve the timing properties through compilation.

State of the art:

- ▶ CompCert observable behavior excludes execution time: *everything the user of the program, or the physical world in which it executes, can “see” about the actions of the program, with the notable exception of execution time and memory consumption;*
- ▶ WCET analysis is not compositional (cf AbsInt): *the analysis of the worst-case execution time is not compositional: given a composition  $A;B$ , the execution time of instruction  $B$  may depend on the state of the architecture after execution of instruction  $A$ .*



# Compilation

## Theorem (Semantic preservation)

$$bh' \in ASem(tp) \Rightarrow \exists bh. bh \in CSem(p) \wedge bh \subseteq bh'$$

where:

- ▶  $ASem$  is semantics of Assembly (target language), and  $CSem$  is semantics of C language (input language);
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where

- ▶  $ASem$  and  $CSem$  extend  $ASem'$  and  $CSem'$  to include timing information, using (co-)monadic semantics;
- ▶  $\subseteq'$  refines the behavior with physical models and timing information.

# Application

White Rabbit experimentally synchronizes clocks at precision below **nano-seconds**. Can we provably certify that such precision holds?

Three layers to formalize:

- ▶ physical layer: time labels are generated by Dual Mixer Time Difference (DMTD).
- ▶ link layer: the Layer-1 Synchronization (SyncE) algorithm extracts a clock frequency from a flow of time stamped messages.
- ▶ network layer: Precise Time Protocol (PTP) synchronises a network with a distributed algorithm. PTP requires a precise source of time, and synchronizes nodes on the network with a precision below nanosecond, in the case of White Rabbit.

## Future work: Type system

Logic to specify rules of time sensitive protocols (e.g., UART).

Related work: LTL types FRP<sup>5</sup>, Hybrid Pi-calculus with session types (ISCAS collaboration).

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Enforcing properties statically, i.e., prove that

$$\vdash P_1 |> N |> P_2 : \phi_{UART}$$

where

- ▶  $P_1$  and  $P_2$  are running programs on the Device 1 and 2 respectively;
- ▶  $N$  specifies the physics of the hardware;
- ▶  $\phi_{UART}$  logically captures the specification of the UART protocol;
- ▶ PAT correspondence: Protocols As Types, Proofs As Terms.

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Existing work on effects and functional programming seems to be a good starting point.